CPU emulators

A quick look on their types, principles of design and operation

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The definition

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Actually, there is no precise definition.

Emulation versus *Simulation*

Many meanings exist. I just interchange both terms.
≪Emulation≫ was introduced in 1957 by IBM and since then it was reinterpreted many times.
In Russian, we usually use simple «Моделирование».

How a simulator is usually designed

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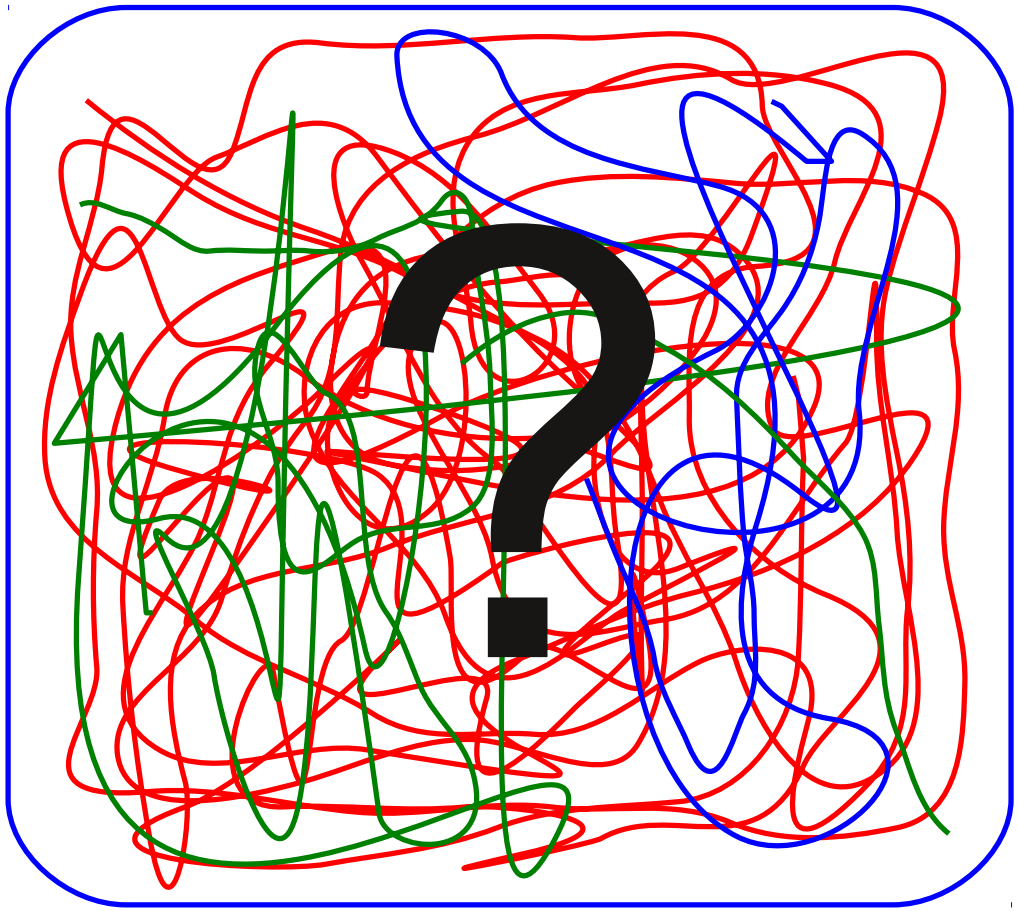

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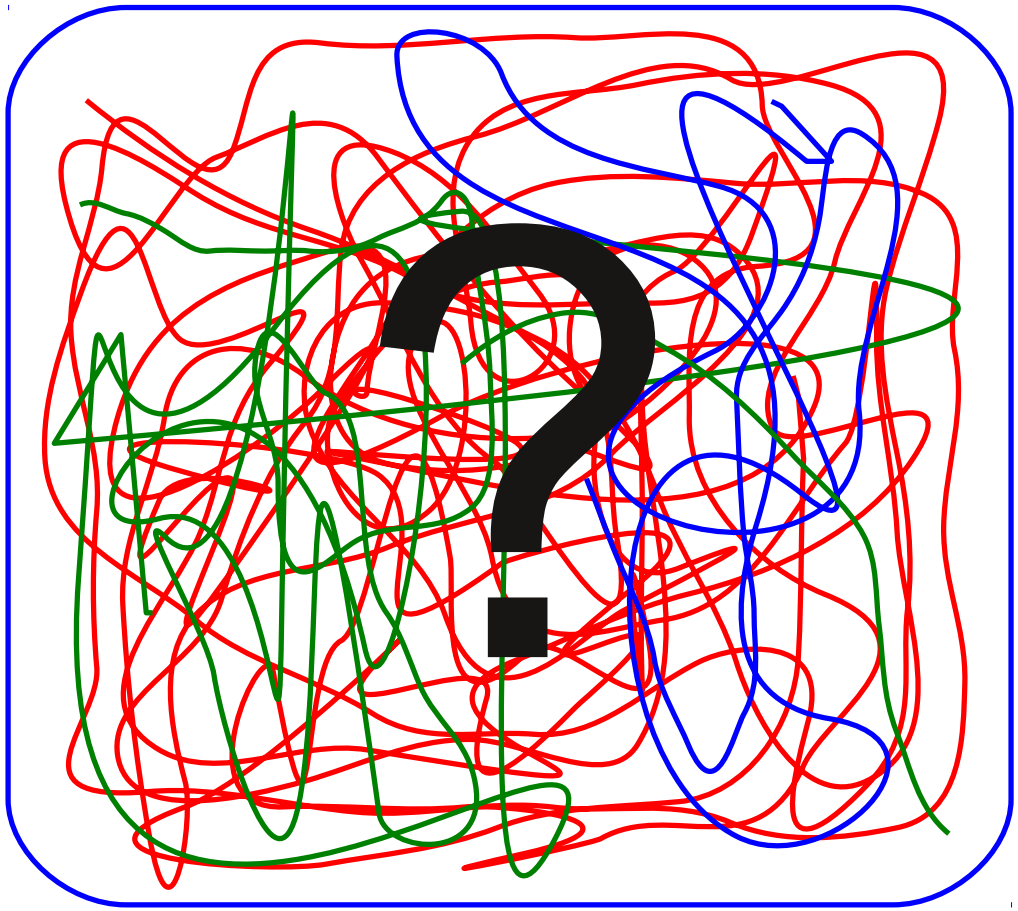
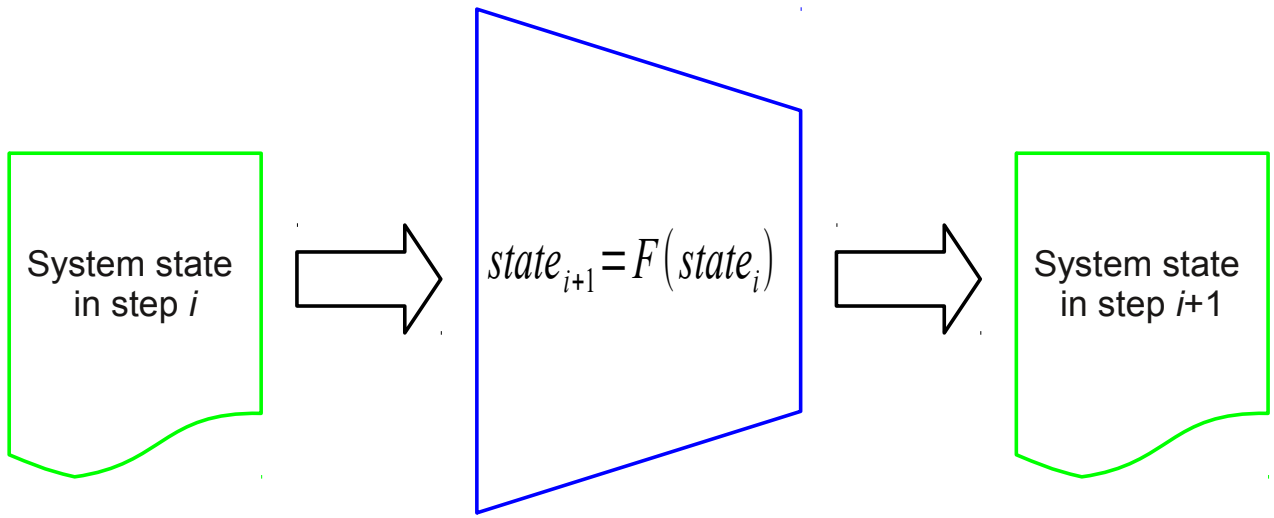


. . . But we can build one step by step!

Functional model

Simulates only the functional features of hardware.



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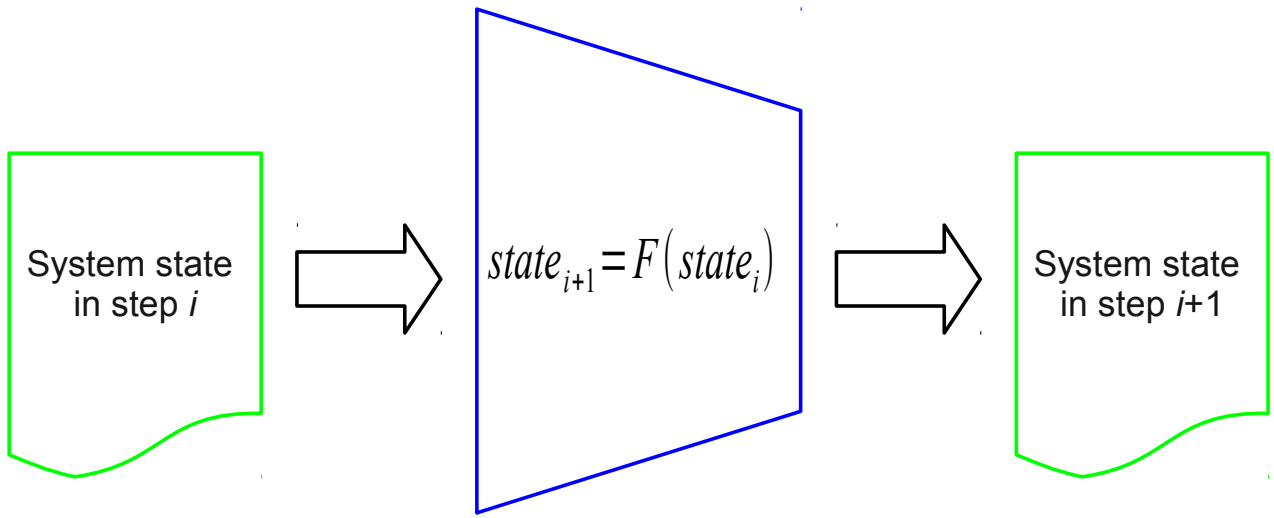
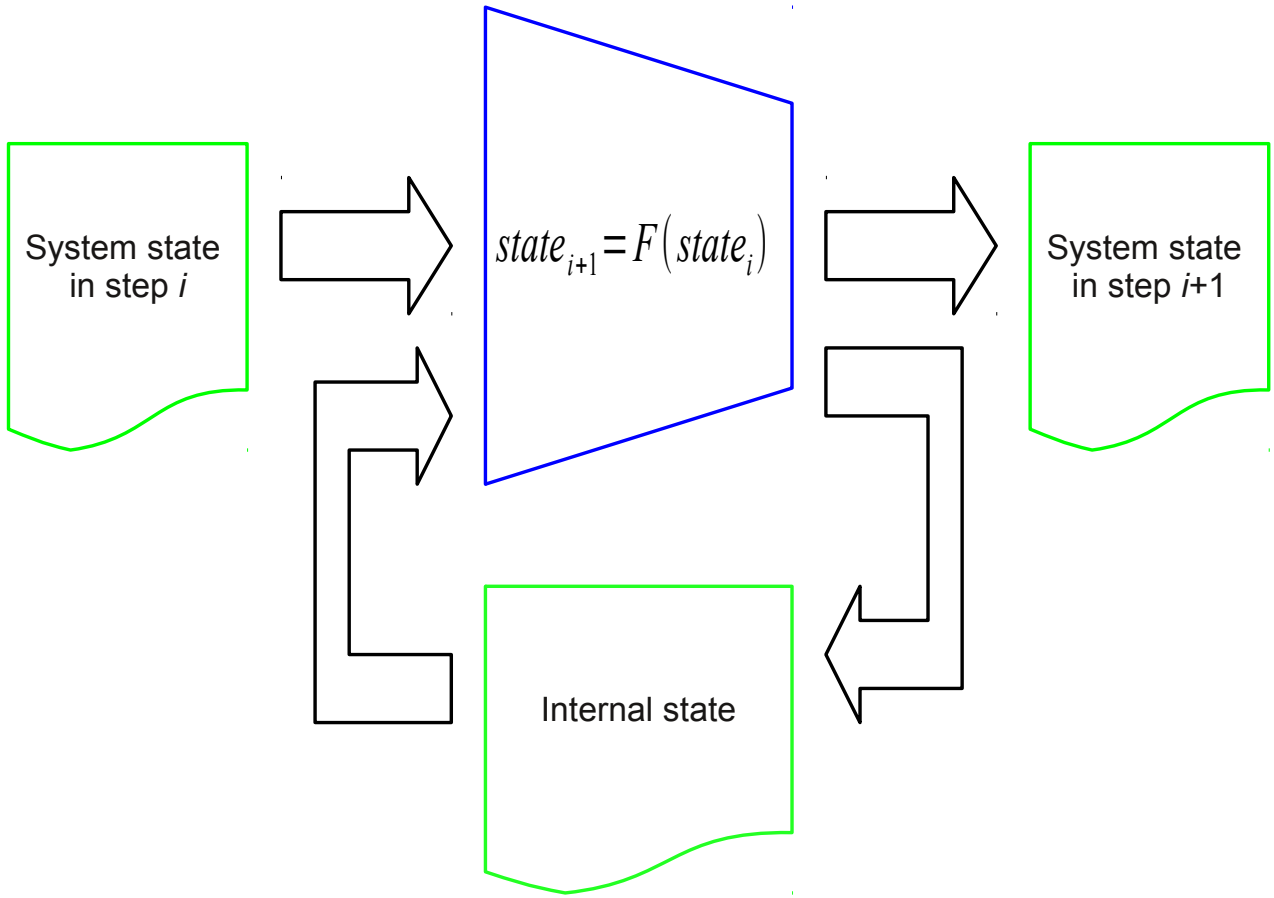


Question: what can be included in system state?

Clock-precise model

Sometimes is called «Performance». Adds a concept of internal temporal state.



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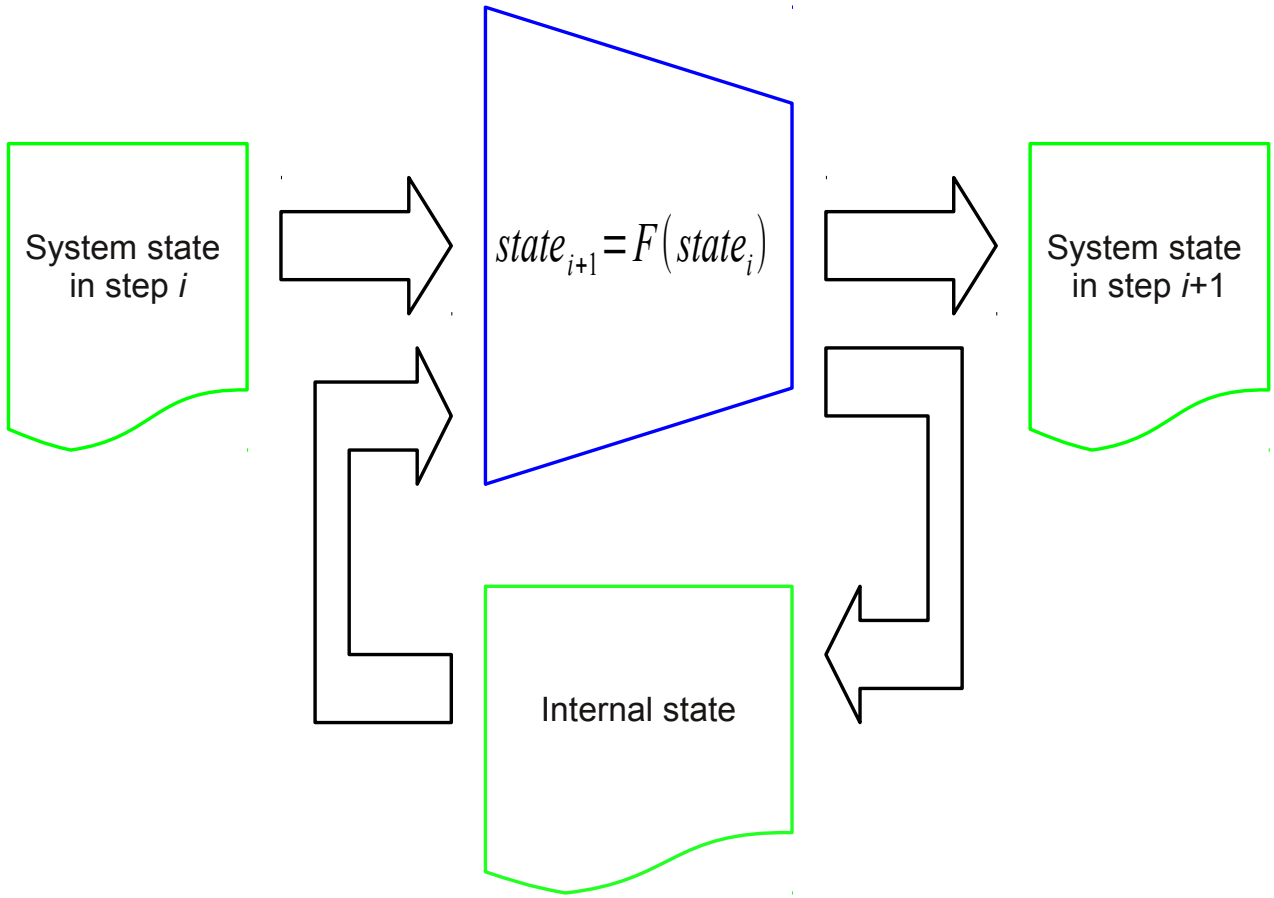

Question: what is in temporal state?

A bit of distraction: a 1 bit summator

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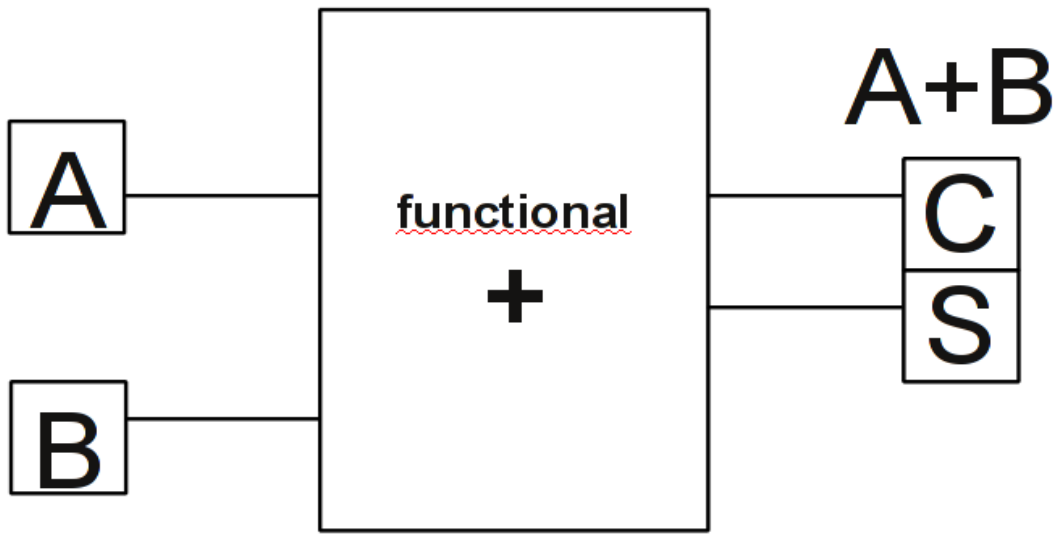

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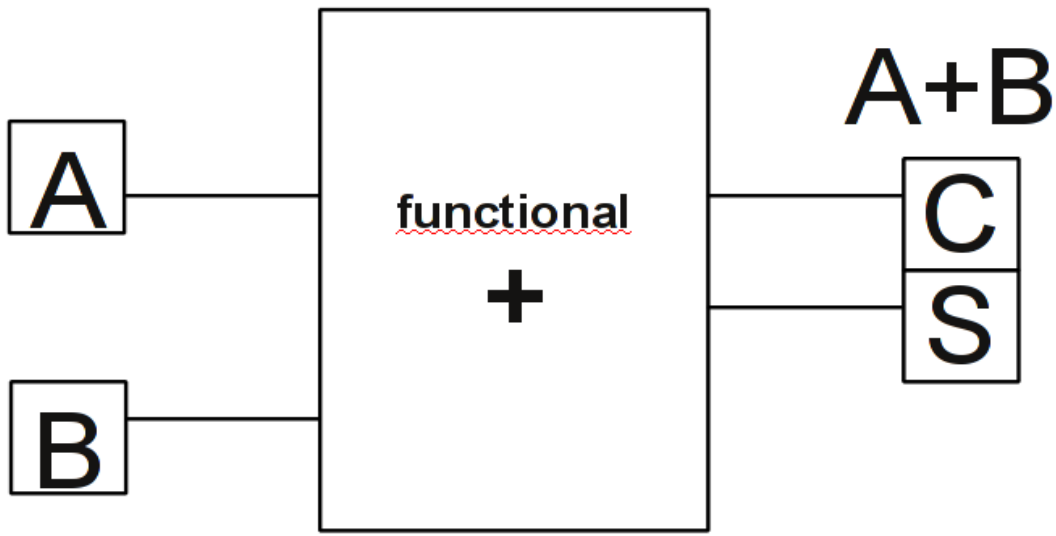

Task: write a table of boolean truth for this adder.

Real 1 bit half-summator

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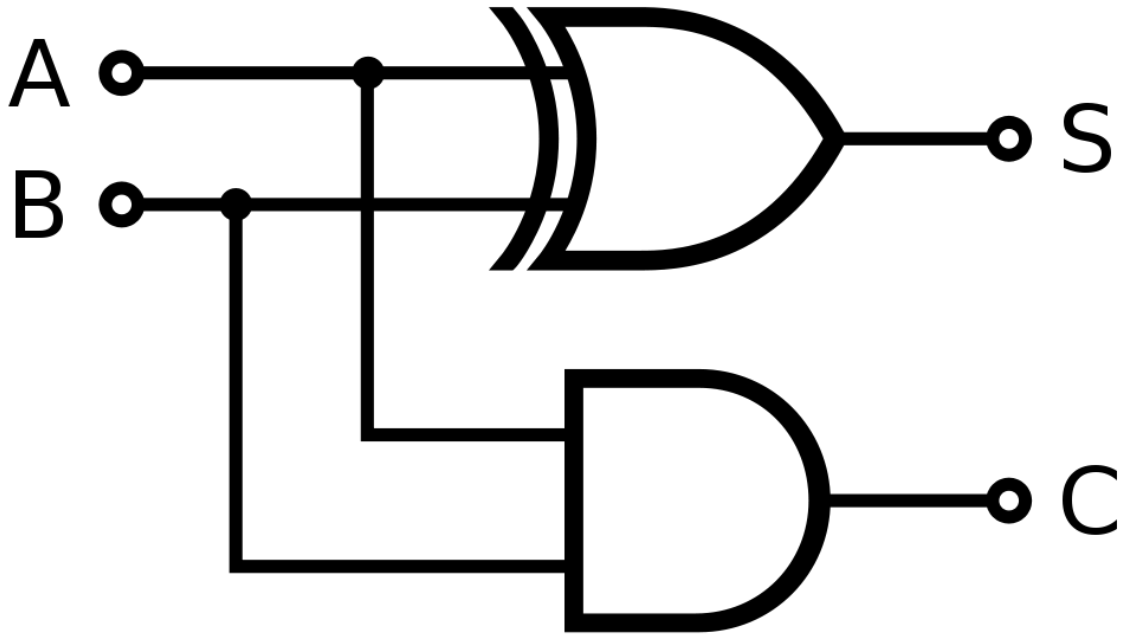

Note: clock synchronization circuitry is not shown.

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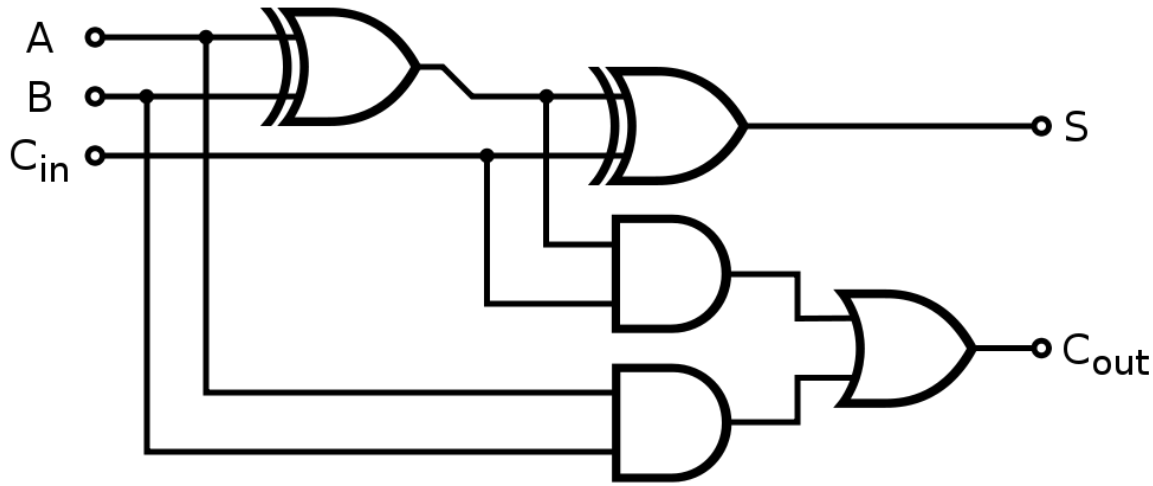

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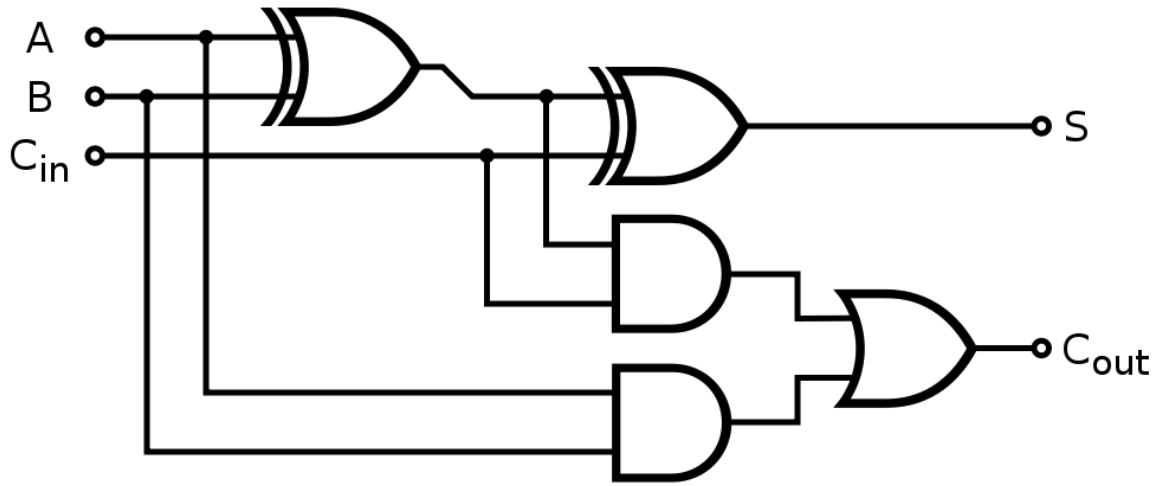
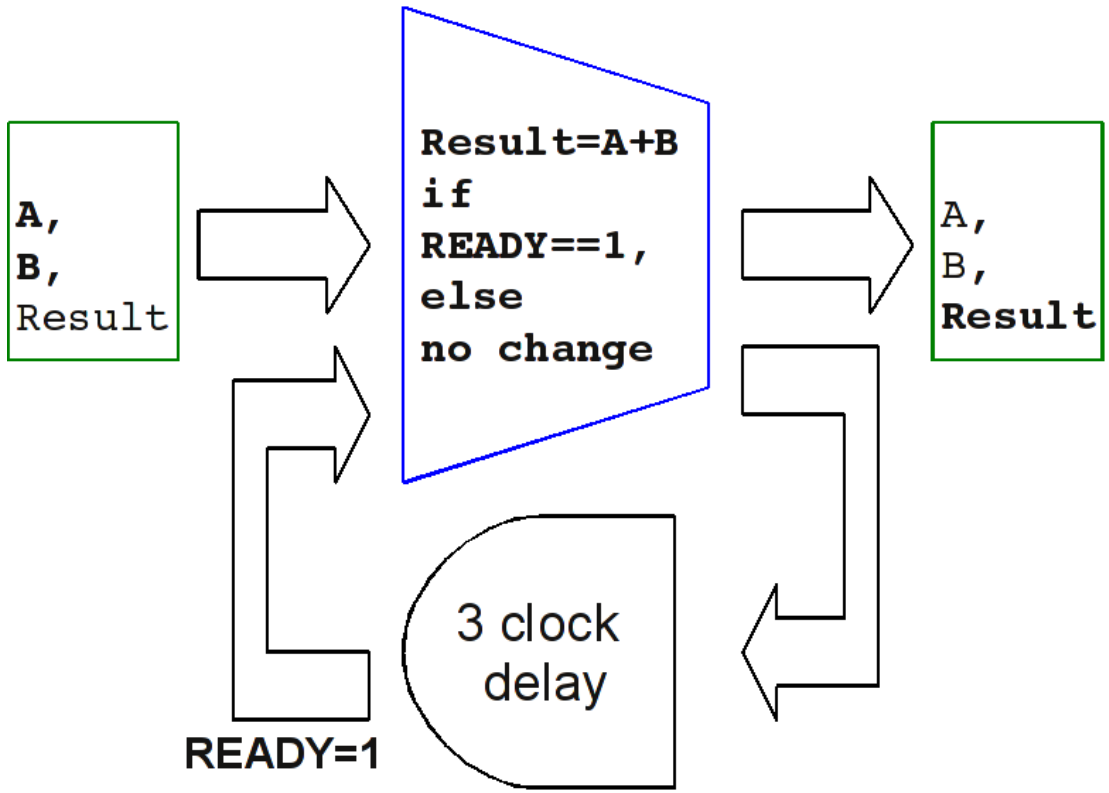


Note: clock synchronization circuitry is not shown.
Question: what type of model this scheme is — functional or clock-precise?

A clock-presise variant of model of full 2 bit adder



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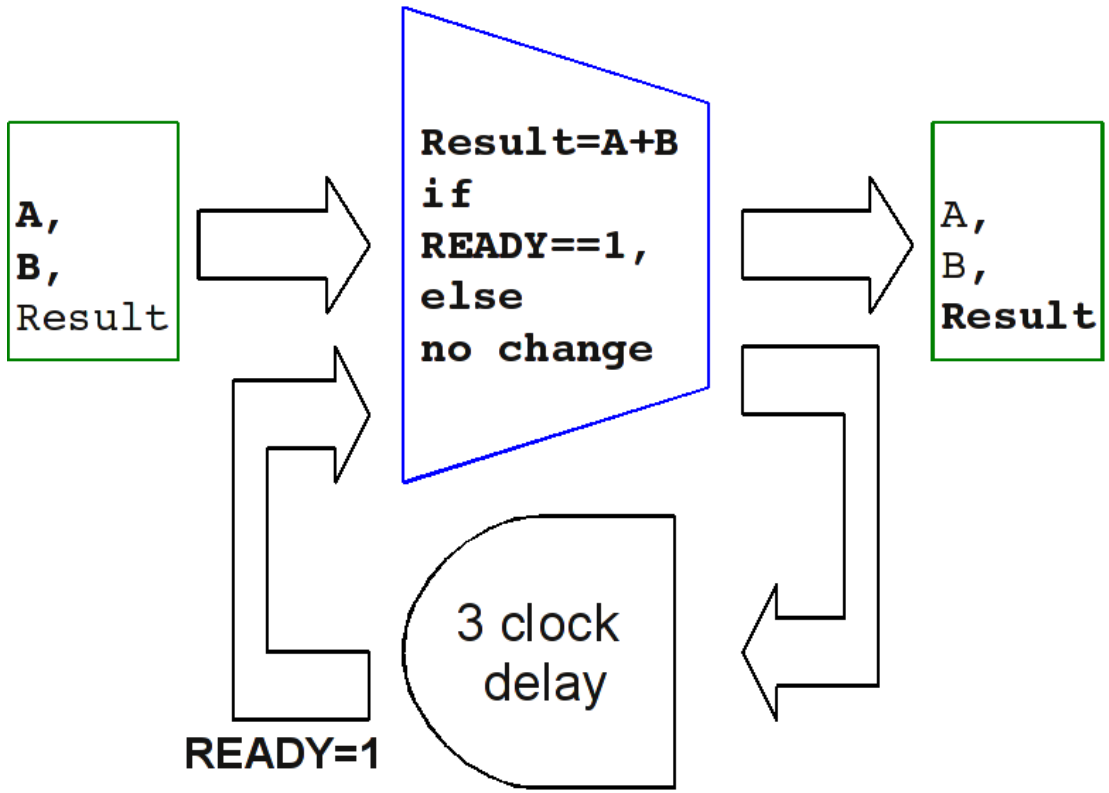

Please note that we don't specify the size of numbers being added or any details of adder. We simply modelled functions **and** timings!

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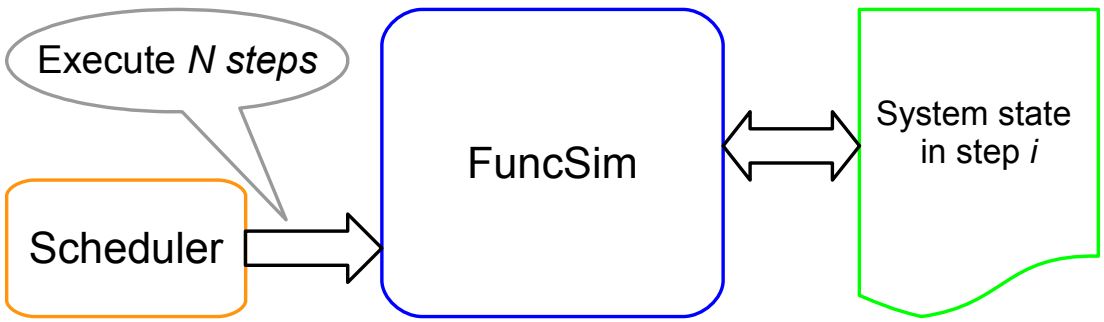

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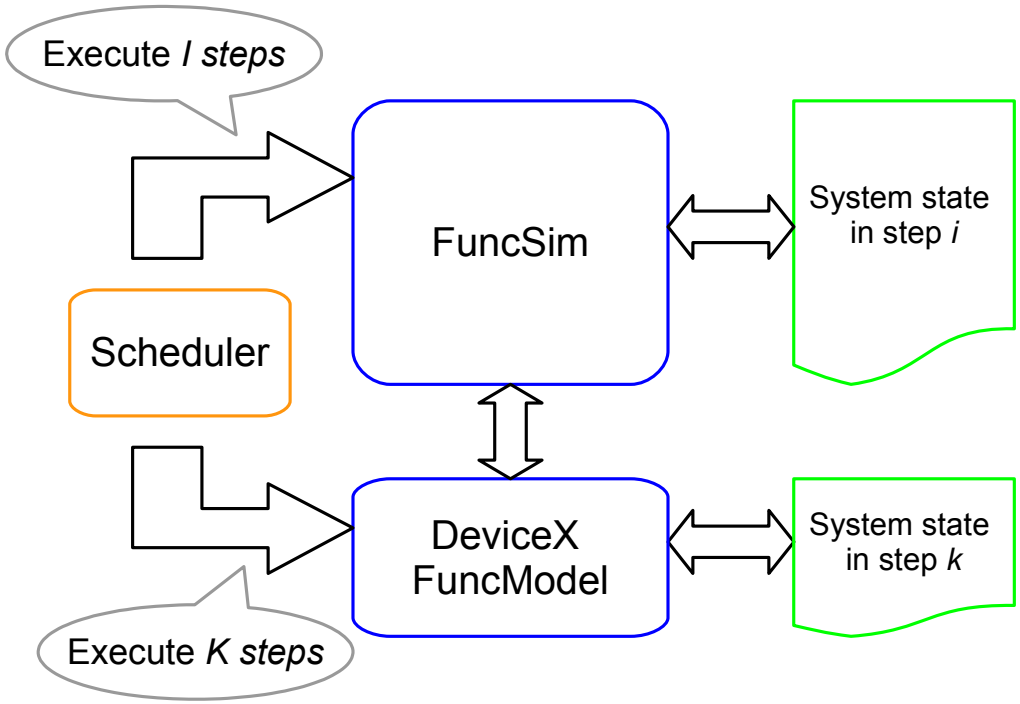

1. Hardly can be used to measure speed, performance, power or certain other parameters of HW.
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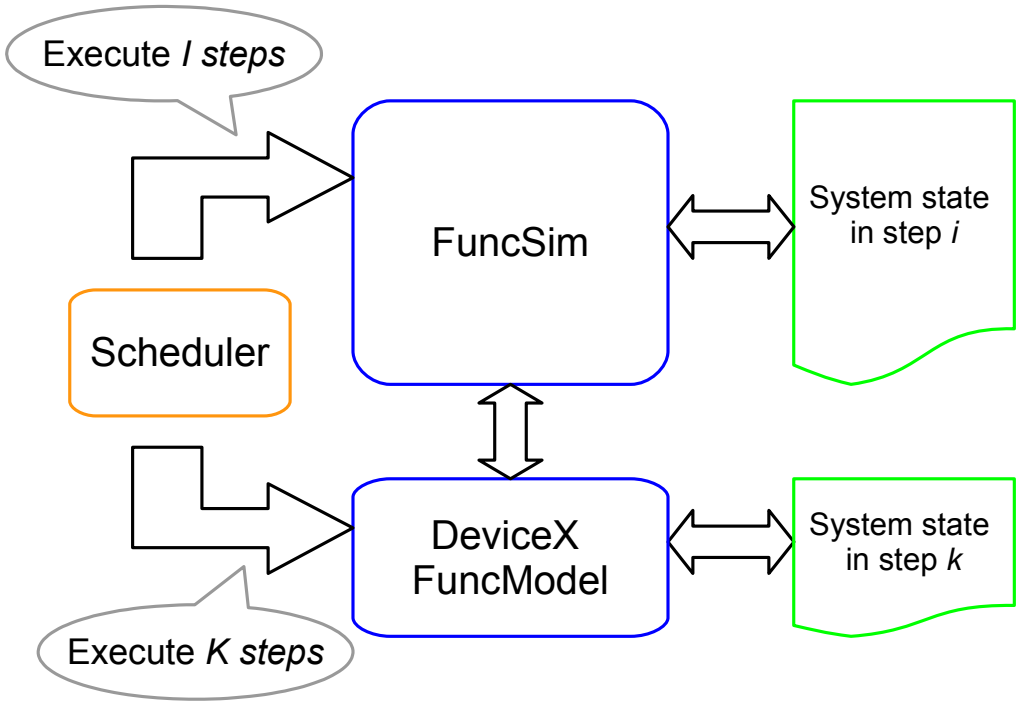

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Another issue is

- Problem of synchronization of all devices' point of view on time.

Types of emulators one more time.

Application mode provide just minimal set of components able to run the particular workloads, e.g. CPU, memory, IO.
Full platform simulates the complete set of HW found in a particular computing system: CPU, memory, network, sound, display, disk, keyboard/mouse...
Hybrid use a junction of software components with ones modeled with hardware models e.g. on FPGA.
Distributed are placed on several computers and interact over the network.

Application mode

- Usually the first one to be implemented for a new architecture.
- Cannot simulate any OS booting (as if there is any OS for the *new* architecture!)
- But applications need an OS to work! Thus such simulator has to implement some minimal non-architectural ABI.
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General steps in making an emulator [4]

- Model the CPU and memory.
- Emulate an instruction set, create disassembler.
- Stub out the rest of the architecture.
- Get basic IO working.
- Work on virtualizing the remaining hardware.

The simpliest CPU emulation [3]

```
for(;;) {
    OpCode = Memory[PC];
    PC++; // program counter
    Counter -= Cycles[OpCode];
    switch(OpCode) {
        case OpCode1:
            Simulate1(); break;
        case OpCode2:
            Simulate2(); break;
        //...
    }
    if(Counter <= 0) {
        // check for interrupts and other tasks
        Counter += InterruptPeriod;
        if (ExitRequired) break;
    }
}
```

Memory emulation

The simplest way to access emulated memory is to treat it as a plain array of items:

```
Data = Memory[Address1];
Memory[Address2] = Data;
```

Such simple memory access is not always possible for following reasons:

- Paged Memory.
- Mirrored Memory.
- ROM protection.
- Memory-Mapped I/O. Accesses to such memory locations produce ≪special effects≫ and therefore should be tracked.

```
Data=ReadMemory(Address1);
WriteMemory(Address2,Data);
```

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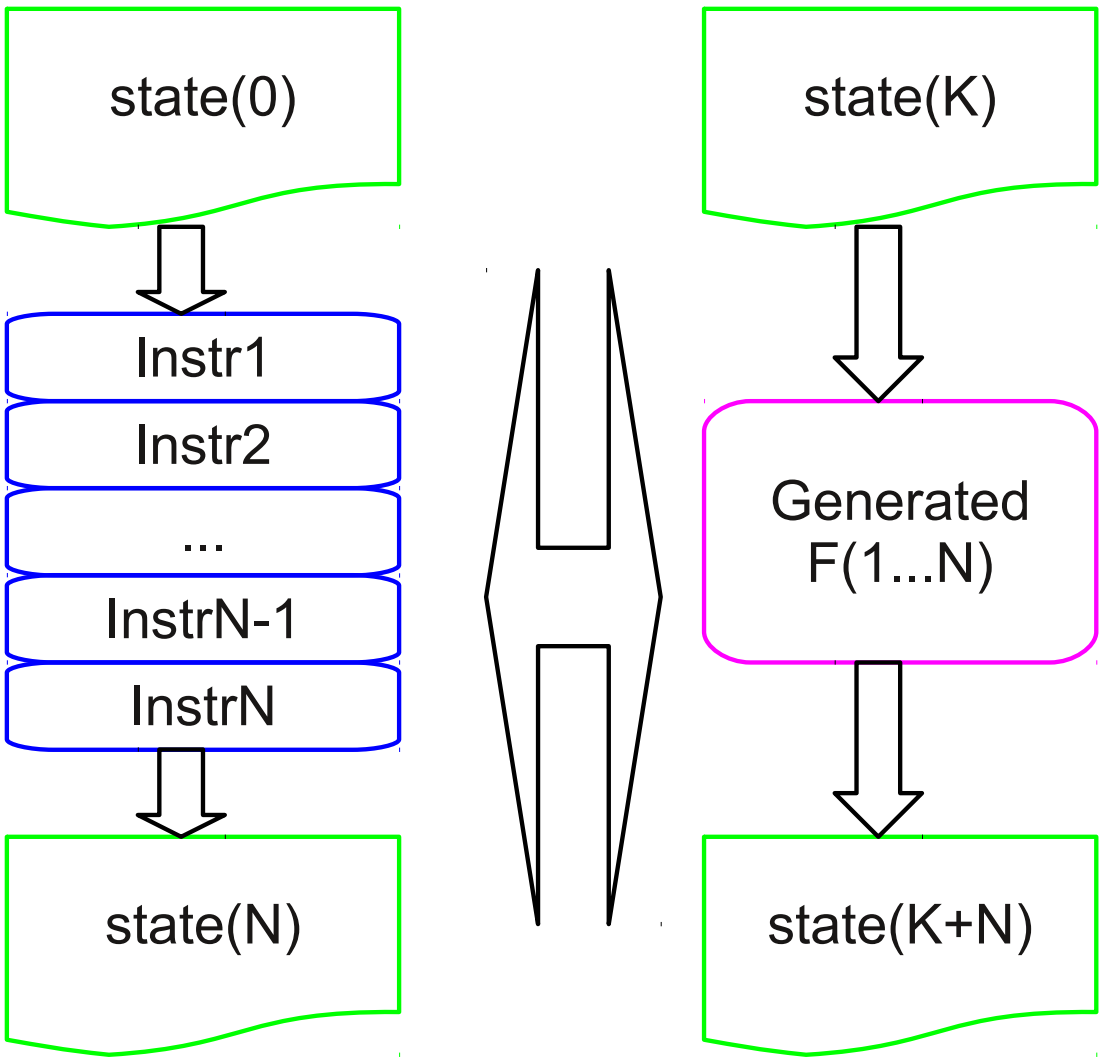
Traces



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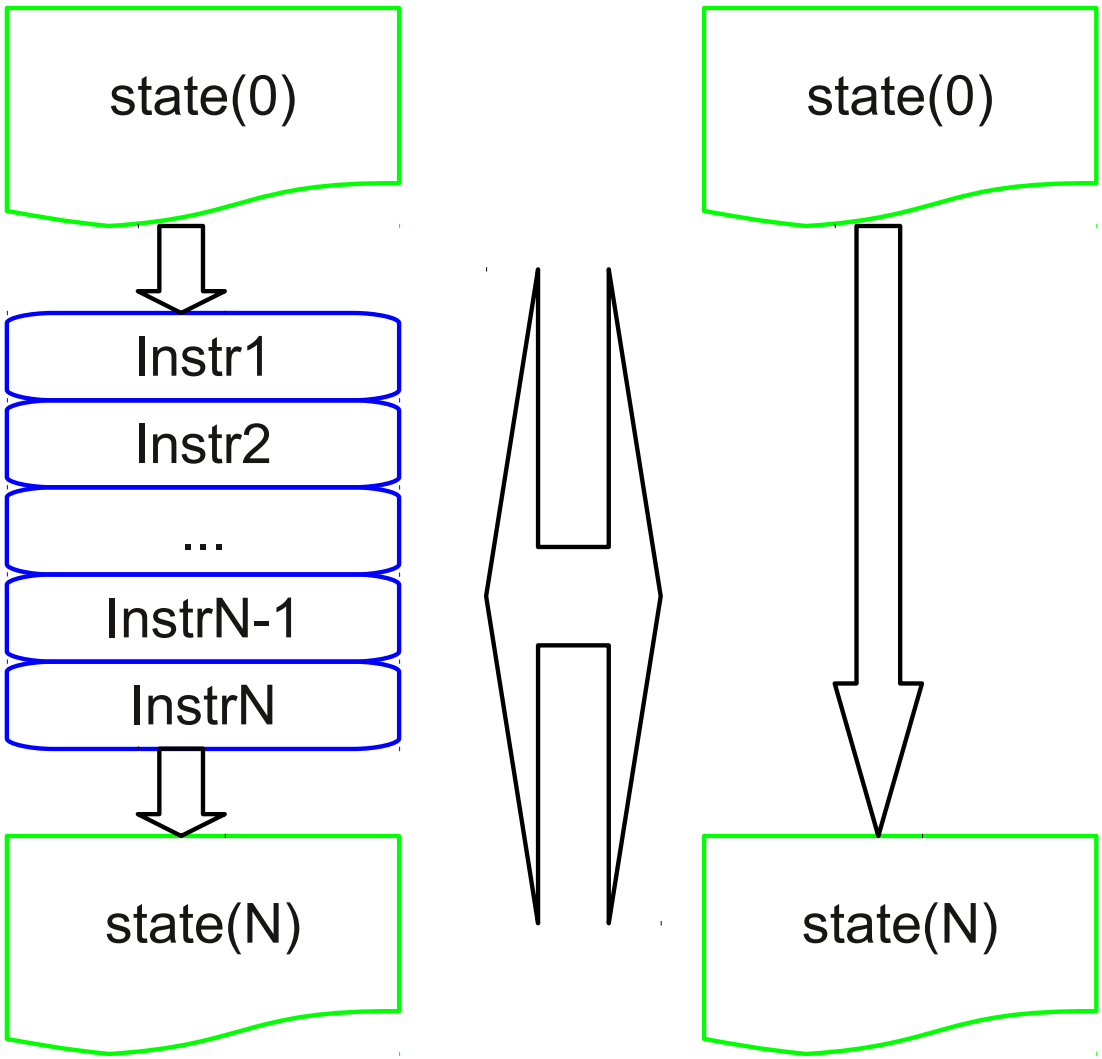
Hyper Simulation



How to boost the speed

Wide range of techniques is used

- Not so lame interpretation: hashing, lookup tables.
- Binary translation.
- Just-in-time compilation.
- Direct execution using virtualization.

Quiz

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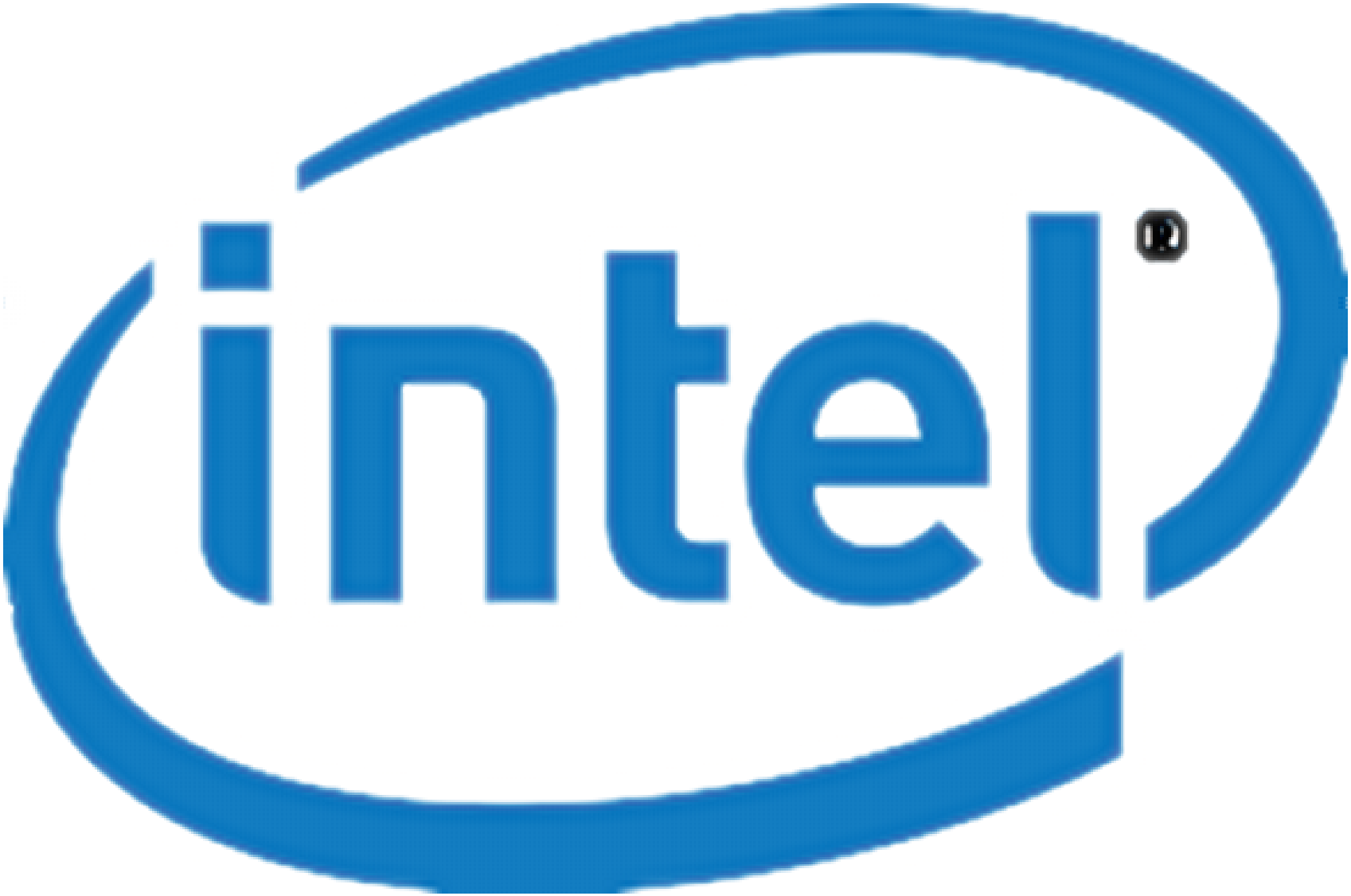
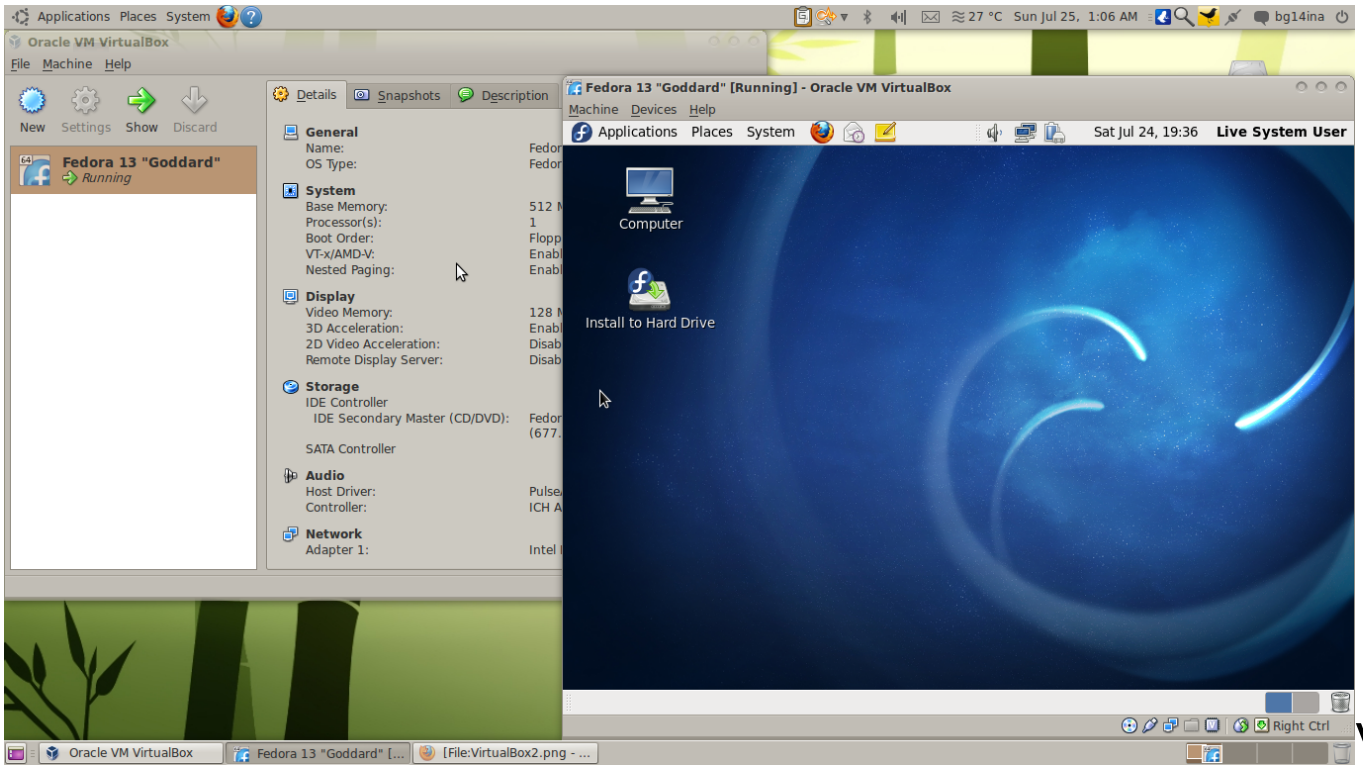
More to read

- Крис Касперски. Техника оптимизации программ. Эффективное использование памяти. СПб. БХВ-Петербург, 2003.
- Wikipedia `http://en.wikipedia.org/wiki/Emulator`
- Marat Fayzullin. How To Write a Computer Emulator `http://fms.komkon.org/EMUL8/HOWTO.html`
- Tony Gray. How to Write an Emulator `http://www.tucs.org.au/how-to-write-an-emulator/`
- Daniel Boris. How Do I Write an Emulator?, Part 1, R1.00
- Carole Dulong et al. The Making of a Compiler for the Intel Itanium Processor. Intel Technology Journal Q3, 2001

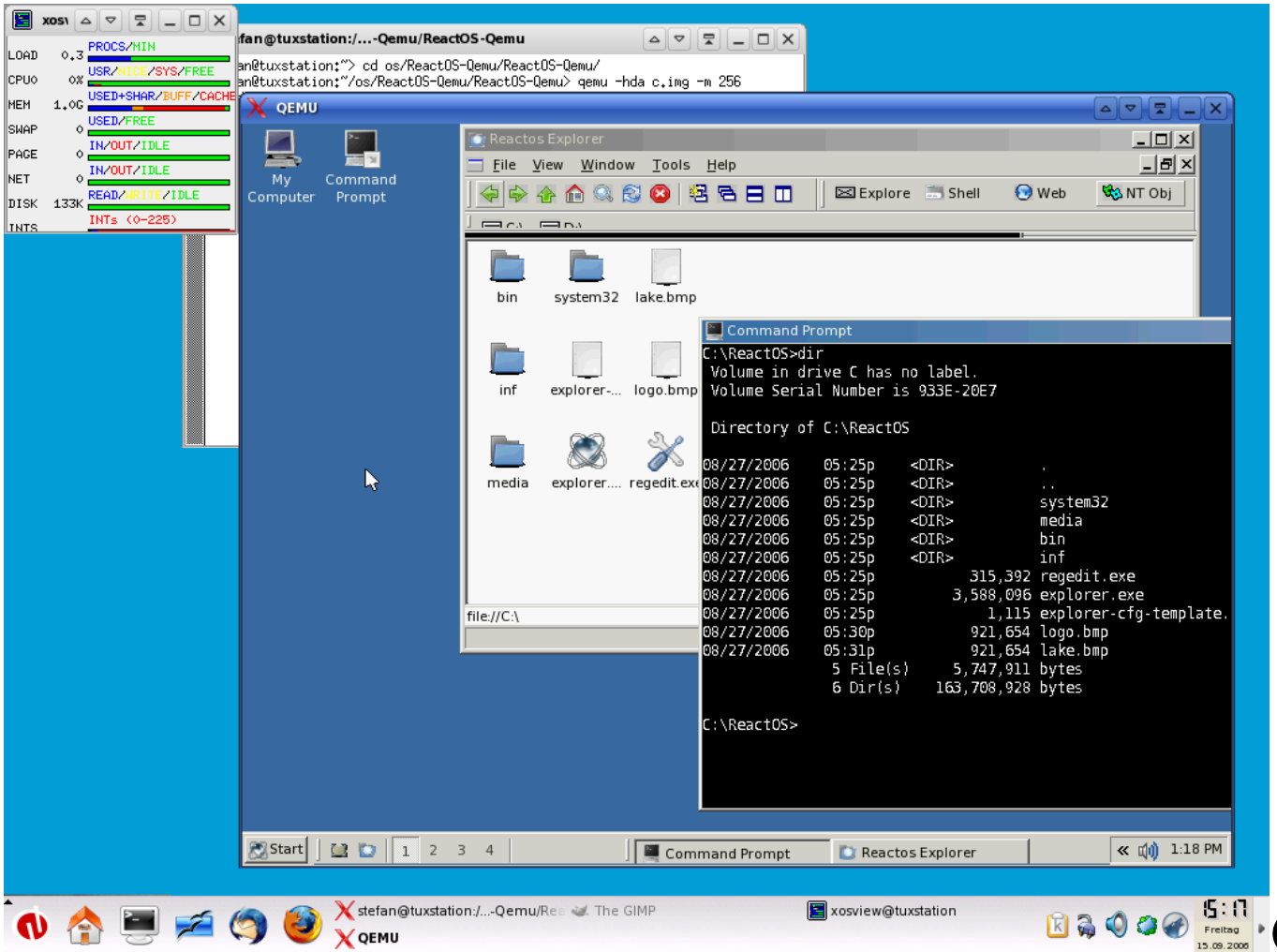

intel

Emulators



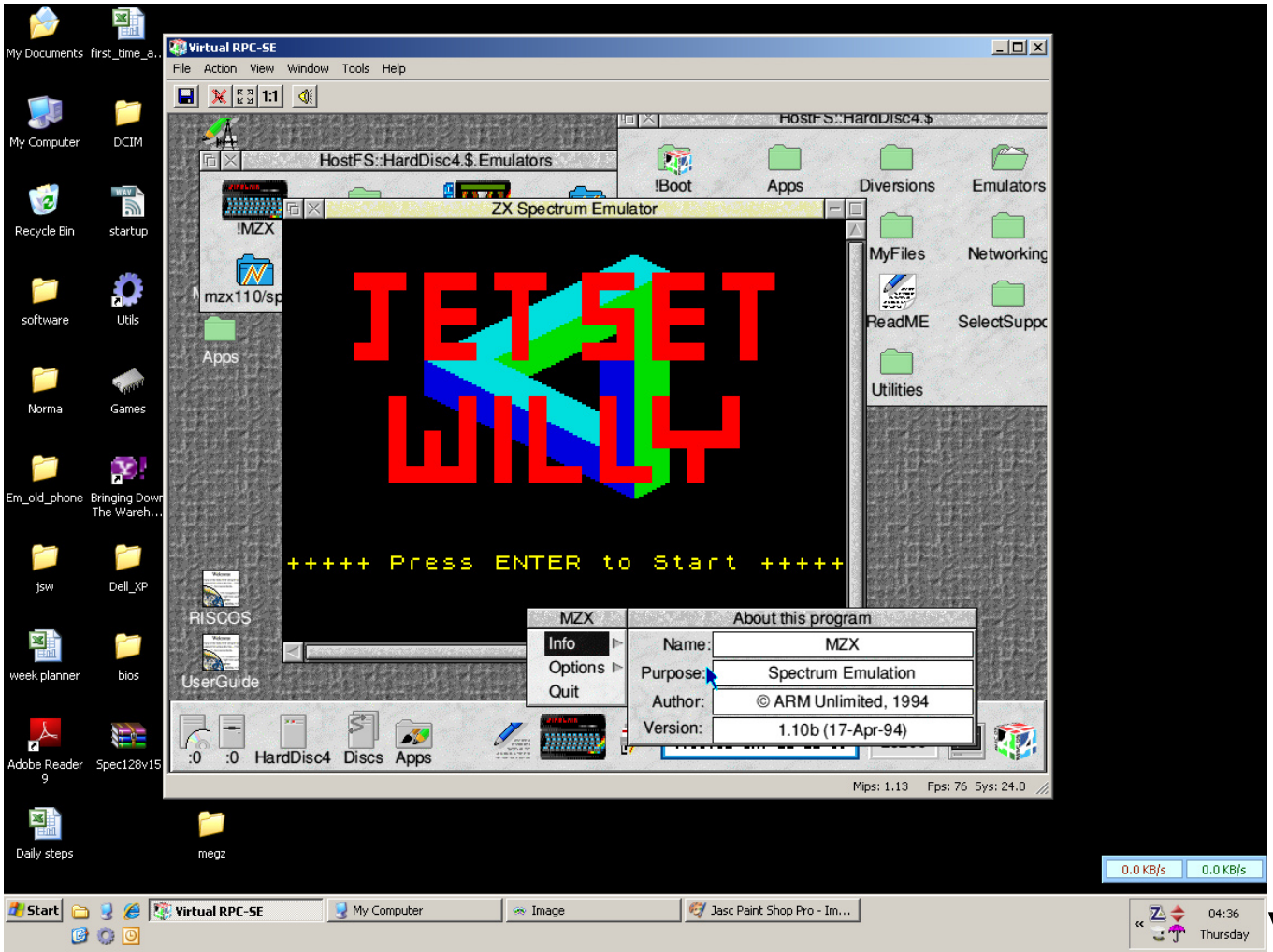
VirtualBox running Fedora 13.

Emulators



Qemu running ReactOS.

Emulators



Virtual RPC running MZX running ZX Spectrum.

Emulators, lots of them! [2]

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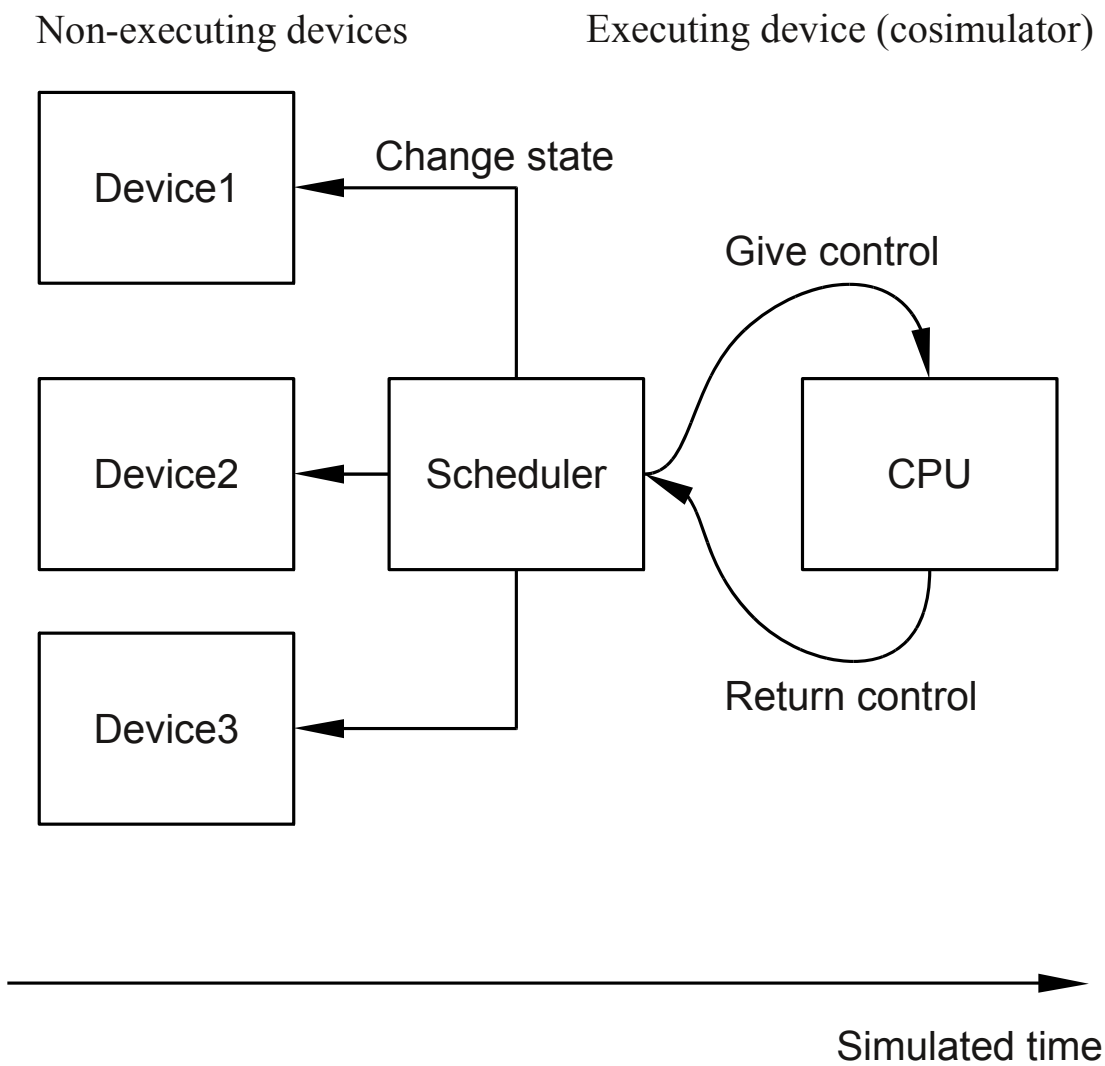
- . . . thousands of them! For every and each of architecture including PDP-10, ZX Spectrum, NES and Itanium.

Simulation of time



Other pecularities

- Simulation of MP systems.
- Endiannes.
- Speed of emulation.